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>Title: **TW0447205B: MULTIPLE NETWORK PROTOCOL ENCODER/DECODER PROCESSOR**

Country: TW Taiwan

Kind: B Patent

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Published / Filed: 2001-07-21 / 1999-11-18

Application Number: TW1999088120194

Number:

IPC Code: H04L 29/06;

ECLA Code: None

Priority Number: 1999-11-18 TW1999088120194

Abstract: A multiple network protocol encoder/decoder comprising a network protocol layer, data handler, O. S. state machine, and memory manager state machines implemented at a hardware gate level. Network packets are received from a physical transport level mechanism by the network protocol layer state machine which decodes network protocols such as TCP, IP, user datagram protocol (UDP), PPP, and raw socket concurrently as each byte is received. Each protocol handler parses and strips header information immediately from the packet, requiring no intermediate memory. The resulting data are passed to the data handler which consists of data state machines that decode data formats such as email, graphics, hypertext transfer protocol (HTTP), Java, and hypertext markup language (HTML). Each data state machine reacts accordingly to the pertinent data, and any data that are required by more than one data state machine is provided to each state machine concurrently, and any data required more than once by a specific data state machine, are placed in a specific memory location with a pointer designating such data (thereby ensuring minimal memory usage). Resulting display data are immediately passed to a display controller. Any outgoing network packets are created by the data state machines and passed through the network protocol state machine which adds header information and forwards the resulting network packet via a transport level mechanism.

? Family:

PDF	Publication	Pub. Date	Filed	Title
<input checked="" type="checkbox"/>	TW0447205B	2001-07-21	1999-11-18	MULTIPLE NETWORK PROTOCOL ENCODER/DECODER AND DATE PROC

1 family members shown above

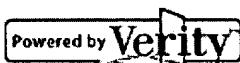
? Other Abstract Info:



None



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